

# Novel Current-Mode All-Pass Filter with Minimum Component Count

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*Abstract*— In this paper, two novel first order currentmode all-pass filters are proposed using a resistor and a grounded capacitor along with a multi-output dual-X second-generation current conveyor (MO-DXCCII). There is no element matching restriction. Both the circuits exhibit low input and high output impedance, which is a desirable feature for current-mode circuits. The proposed circuits are simulated using SPICE simulation program to confirm the theory.

*Index Terms*— Analog signal processing, DXCCII, current-mode, all-pass filter.

# I. INTRODUCTION

Current-mode (CM) circuits have become popular due to their advantages such as wider dynamic range, inherent wider bandwidth, simple circuitry and low power consumption over the voltage-mode counterparts [1]. On the other hand, a CM circuit with features of low input and high output impedance is of special interest as there is no need for additional circuitry or active element for cascading or interfacing within a system and it also decreases the number of active elements in the system. First order all-pass filters (APF) are very useful building block for many analog signal processing applications such as for the realization of multiphase oscillators, high Q band pass filters, phase equalizers and also for introducing frequency dependent delay while keeping the amplitude of the input signal constant over a frequency range of interest. Recently, the design of first-order current-mode all-pass filter (CMAPF) has received attention for the use of minimal number of active and passive components (i.e. one active element and two passive components).

Numerous first-order CMAPF circuits were proposed using different types of current-mode active building blocks in the literature [2-15]. Some of these circuits are based on single active element [3-5, 7-10, 12, 14]. The reported single active element based circuits as described in [3-5, 7, 10, 12] employ more than two passive components, but show the feature of high output impedance suited for CM cascading. Moreover the circuits described in [6, 11, 13-14] fall in category of tunable, resistorless realizations. The most recent of these described in [14] shows low input and high output impedance feature with single active element and one capacitor. The circuit described in [8] employs one current differencing buffered amplifier (CDBA), one capacitor and one resistor. However, its input impedance is not low and the capacitor used is not grounded. The circuit given in [2] employs two active elements. The circuits presented in [5, 9] are based on minimal component count, but require additional current conveyor to sense the output current. The recently published all-pass filter in [15] employs two active elements and two passive components, but exhibits low input and high output impedances.

In this paper, two new CMAPFs employing a single multi-output dual-X second-generation current conveyor (MO-DXCCII) are presented. The proposed circuits employ only one resistor and one capacitor and require no element matching restriction. Each circuit possesses low input and high output impedance. The proposed circuits consist of one lesser active element in comparison to paper [15]. To support the theory, PSPICE simulation results using TSMC 0.35µm CMOS parameters are included.

The paper is organized as follows: in section II, the description of DXCCII with its port relationship is presented. The proposed all-pass filters using MO-DXCCII are described in section III. In section IV, parasitic study and non-ideal analysis of the proposed circuits are given. In section V, to verify the theoretical study, the first order current-mode all-pass filters are designed and simulated with SPICE simulation program. Finally, the conclusion is presented in section VI.

# II. MULTI-OUTPUT DUAL-X SECOND-GENERATION CURRENT CONVEYOR (DXCCII)

Dual-X second generation current conveyor (DXCCII) [16] is a relatively recently introduced useful and versatile current-mode active element for analog signal processing applications [17-20]. It encompasses the advantages of second generation current conveyor (CCII) and inverting second generation current conveyor (ICCII). The



Figure 1. CMOS Implementation of MO-DXCCII [16]

practical realizations of this active element have been successfully explored using commercially available ICs [21]. The CMOS implementation of multi-output dual-X second-generation current conveyor (MO-DXCCII) is shown in Fig. 1. The port relationship of the MO-DXCCII can be characterized as:

$$\begin{bmatrix} I_{Y} \\ V_{X+} \\ V_{X-} \\ I_{Z1+} \\ I_{Z2+} \\ I_{Z1-} \\ I_{Z2-} \end{bmatrix} = \begin{bmatrix} 0 & 0 & 0 \\ 1 & 0 & 0 \\ -1 & 0 & 0 \\ 0 & 1 & 0 \\ 0 & 0 & 1 \\ 0 & 0 & 1 \end{bmatrix} \begin{bmatrix} V_{Y} \\ I_{X+} \\ I_{X-} \end{bmatrix}$$
(1)

#### **III. PROPOSED CIRCUITS**

The proposed CM-APFs are shown in Fig. 2. Both the circuits employ a single MO-DXCCII, and the minimal number of passive components (one resistor and one capacitor). Note that the capacitor used is in grounded form, which makes the circuits suitable for integrated circuit implementation. Routine analysis of the circuits of Fig. 2 yields the following current transfer function

CMAPF-II: 
$$\frac{I_{OUT}}{I_{IN}} = -\left(\frac{s - \frac{1}{(C + C_{Z1-})(R + R_{X+})}}{s + \frac{1}{(C + C_{Z1-})(R + R_{X+})}}\right)$$
(2)

The frequency dependent phase response  $(\Phi)$  of the filters is given by

$$\phi(\omega) = -2\tan^{-1}(\omega CR) \tag{3}$$



Figure 2. (a) CM APF-I (b) CM APF-II

It is to be noted from (2) that no passive element matching constraints are required for the proposed circuits. The proposed filters exhibit low input and high output impedance, which make them suitable for cascading without requiring additional current buffer circuit. The other salient features of the two proposed circuits are use of single active element and two passive components. The three features are not exhibited together in any of the available work [2-15]. As far as integrated aspect of the new proposed circuits are concerned, the resistor can be replaced by active-MOS resistor with the added advantage of tunability through external voltage [22] and the capacitor can be implemented in MOS technology [23].

## IV. NON-IDEAL ANALYSIS AND EFFECTS OF MO-DXCCII PA RASITIC

## A. Non-ideal Analysis

Taking non-idealities of the MO-DXCCII into account, the port relationships of the voltages and currents modify to

$$\begin{bmatrix} I_{Y} \\ V_{X+} \\ V_{X-} \\ I_{Z1+} \\ I_{Z2+} \\ I_{Z1-} \\ I_{Z2-} \end{bmatrix} = \begin{bmatrix} 0 & 0 & 0 \\ \beta_{1} & 0 & 0 \\ -\beta_{2} & 0 & 0 \\ 0 & \alpha_{1} & 0 \\ 0 & \alpha_{2} & 0 \\ 0 & 0 & \alpha_{3} \\ 0 & 0 & \alpha_{4} \end{bmatrix} \begin{bmatrix} V_{Y} \\ I_{X+} \\ I_{X-} \end{bmatrix}$$
(4)

Here,  $\alpha_1$  and  $\alpha_2$  are the current transfer gains from X+ terminals to Z1+ and Z2+ terminals, respectively  $\alpha_3$  and  $\alpha_4$  are the current transfer gains from X- terminals to Z1and Z2- terminals, respectively and  $\beta_1$  and  $\beta_2$  are the voltage transfer gains from Y input terminal to X+ and X- terminals, respectively. However, these transfer gains are close to unity up to a very high frequency range (i.e. in GHz) [8].

The proposed circuits are reanalyzed assuming nonideal MO-DXCCII; the modified current transfer functions are found as

CMAPF-I: 
$$\frac{I_{OUT}}{I_{IN}} = -\alpha_2 \left[ \frac{s + \frac{\alpha_2 - \alpha_3 - \alpha_4}{CR\alpha_2}}{s + \frac{1}{CR}} \right]$$
(5)

CMAPF-II: 
$$\frac{I_{OUT}}{I_{IN}} = -\alpha_4 \left[ \frac{s + \frac{\alpha_4 - \alpha_1 - \alpha_2}{CR\alpha_4}}{s + \frac{1}{CR}} \right]$$
(6)

From (5) and (6), it is to be observed that the nonidealities do affect the filter gain, as it now depends on  $\alpha_2$ and  $\alpha_4$  for the CMAPF-1 and CMAPF–II, respectively but the pole frequency is unaltered by MO-DXCCII nonidealities. The pole-frequency ( $\omega_0$ ) and gain (*H*) sensitivity for the all-pass filter circuits to the non-idealities as well as external components is analyzed and is tabulated in Table I.

From Table I, it is to be observed as the sensitivities of active and passive components with respect to pole frequency ( $\omega_0$ ) and gain (*H*) are less than or equal to unity in magnitude meaning good sensitivity performance.

TABLE I. SEN SITIVITY FIGURES WITH RESPECT TO POLE FREQUENCY  $(\omega_o)$  AND GAIN (H) FOR THE PROPOSED CIRCUITS OF FIG. 2

Circuit	$S^{\omega_o}_{lpha_1,lpha_2,lpha_3,lpha_4,eta_1,eta_2}$	$S_{R,C}^{\omega_o}$	$S^{H}_{\alpha_{2}}$	$S^{\scriptscriptstyle H}_{lpha_4}$	$S^{H}_{\alpha_{1},\alpha_{3},\beta_{1},\beta_{2},R,C}$
CMAPF-I	0	-1	1	0	0
CMAPF-II	0	-1	0	1	0

#### B. Effects of MO-DXCCII Parasitic

Effects of parasitics involved with the used current conveyor are now considered. The various parasitic involved with a typical MO-DXCCII are a small parasitic resistances  $R_{X+}$  and  $R_{X-}$  at X+ and X- terminals respectively, parasitic at Y terminal is  $(R_Y//C_Y)$  and parasitic at Z1+, Z2+, Z1- and Z2- terminals are  $(R_{Z1+}//C_{Z1+}), (R_{Z2+}//C_{Z2}), (R_{Z1-}//C_{Z1-}) \text{ and } (R_{Z2-}//C_{Z2-}),$ respectively [24]. The parasitic model of MO-DXCCII is also shown in Fig. 3. Here, it is assumed that the circuits are restricted to work at frequencies much lower than the corner frequencies of  $\beta_i$  (i = 1, 2) and  $\alpha_i$  (i = 1, 2, 3, 4). But practically it is assumed that the external resistors are much smaller than the parasitic resistors at the Y and Z terminals of MO-DXCCII, i.e.  $R \ll R_{\rm Y}$  or  $R_{\rm Z1+}$  or  $R_{\rm Z2+}$  or  $R_{Z1-}$  or  $R_{Z2-}$  and the external resistors are much greater than the parasitic resistors at the X+ and X- terminals of MO-DXCCII, i.e.  $R_{X+}$  or  $R_{X-} \ll R$ . A re-analysis of the two circuits given in Fig. 2 yields the following transfer function

CMAPF-I: 
$$\frac{I_{OUT}}{I_{IN}} = -\left(\frac{s - \frac{1}{(C + C_{Zi})(R + R_{x-})}}{s + \frac{1}{(C + C_{Zi})(R + R_{x-})}}\right)$$
 (7)

CMAPF-II: 
$$\frac{I_{OUT}}{I_{IN}} = -\left(\frac{s - \frac{1}{(C + C_{ZL})(R + R_{X+})}}{s + \frac{1}{(C + C_{ZL})(R + R_{X+})}}\right)$$
 (8)

From (7) and (8), the parasitic resistances / capacitances get absorbed with the external values. Because the X terminal is connected directly to a resistor so the parasitic resistance at the X terminal of the MO-DXCCII ( $R_X$ ) can be absorbed as a part of resistance (R), where as the parasitic capacitances ( $C_{Z1-}$  or  $C_{Z1+}$ ) is in shunt with capacitor (C). This causes slight deviation in circuit's parameters, which can be minimized by pre-distorting the designed values to be used in the circuits. It is also seen that the pole-frequency would be in error because of these parasitic. The error is expected to be small for an integrated MO-DXCCII; the actual value to be given in the 'simulation results'.



Figure 3. Parasitic Model of MO-DXCCII

### V. SIMULATION RESULTS

The proposed circuits are verified using the PSPICE simulation program. The MO-DXCCII is realized by the CMOS implementation [16] of Fig. 1 using 0.35  $\mu$ m TSMC process parameters (Table II). The aspect ratios of NMOS and PMOS transistors are chosen as in Table III and the supply voltages used were  $\pm 1.8$  V and V<sub>BB</sub> = -1.25 V. The CMAPF-I is designed with C = 100 pF and R = 1 K $\Omega$  for a theoretical pole frequency of 1.59 MHz The phase and gain responses for the CMAPF-I are shown plotted in Fig. 4, which shows a constant unity gain and frequency dependent phase. A phase shift of -90 is obtained at the pole frequency and the pole frequency is found to be as 1.51 MHz, which is in consistency with the designed value. Next figure (Fig. 5) shows the time domain input and output waveforms, along with their Fourier spectrum when a sinusoidal input signal of frequency 1.59 MHz is applied at the input terminal of the circuit. The output current is  $-90^{\circ}$  phase shifted with respect to the input, which verifies circuit operation as a phase shifter. In addition, the Lissajous pattern for the circuit as  $-90^{\circ}$  phase shifter is shown plotted in Fig. 6. The variation of the total harmonic distortion (THD) at the output by varying the amplitude of the sinusoidal input current at 1.59 MHz is shown in Fig. 7. It can be seen that the THD value of the filter increases for the input current in excess to 200  $\mu$ A (peak to peak). The simulations confirm the proposed circuits and results agree with the theory.

#### VI. INTEGRATION ASPECTS

The integration prospects of the new proposed circuits are considered here. The proposed current-mode first order all-pass filters employ single MO-DXCCII, which can be implemented in CMOS technology [16]. Both the circuits use a single grounded capacitor in its realization, which is suitable for integration. There are techniques to implement the capacitor in MOS technology [23, 25]. The resistor used in the circuits can be replaced by active MOS resistor, available in literature [22], with an added feature of tunability by control voltage. Thus the proposed circuits are suitable for IC implementation.

TABLE II. 0.35 µm TSMC CMOS PROCESS PARAMETERS

NMOS:				
LEVEL=3	TOX=7.9E-9	NSUB=1E17		
GAMMA=0.582787	1 PHI=0.7 VTO=0.5	445549 DELTA=0		
UO=436.256147 EI	A=0 THETA=0.17496	584 KP=2.055786E-		
4 VMAX=8.309444	E4 KAPPA=0.25740	81 RSH=0.0559398		
NFS=1E12 TP	G=1 XJ=3E-7	LD=3.162278E-11		
WD=7.04672E-8	CGDO=2.82E-10	CGSO=2.82E-10		
CGBO=1E-10 CJ	=1E-3 PB=0.975853	33 MJ=0.3448504		
CJSW=3.777852E-1	10 MJSW=0.3508721			
PMOS:				
LEVEL =3	TOX = 7.9E	-9 NSUB=1E17		
GAMMA=0.408389	4 PHI=0.7 VΓO=-0.7	7140674 DELTA=0		
UO=212.2319801	ETA=9.999762E-4	THETA=0.2020774		
KP=6.733755E-5	VMAX=1.181551E	5 KAPPA=1.5		
RSH=30.0712458	NFS=1E12 TF	'G=-1 XJ=2E-7		
LD=5.000001E-13	WD=1.249872E-7	CGDO=3.09E-10		
CGSO=3.09E-10	CGBO=1E-10	CJ=1.419508E-3		
PB=0.8152753 MJ=0.5 CJSW=4.813504E-10 MJSW=0.5				

TABLE III. TRANSISTOR ASPECT RATIOS FOR THE CIRCUIT SHOWN IN FIG. 1.

Transistors	W(µm)	L(µm)
M <sub>1</sub> -M <sub>2</sub>	1.4	0.7
M <sub>3</sub> -M <sub>5</sub>	2.8	0.7
M <sub>17</sub> -M <sub>18</sub>	2.4	0.7
M <sub>19</sub> -M <sub>21</sub>	4.8	0.7
M <sub>6</sub> -M <sub>16</sub> , M <sub>22</sub> -M <sub>24</sub>	9.6	0.7



Figure 4. Simulated gain and phase response of CM APF-I



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Figure 5. (a) Input/output responses (b) Fourier spectrum at 1.59MHz



Figure 6. Lissajous pattern showing -90° phase shift at pole frequency



1.59 MHz

## VII. CONCLUSION

This paper presents two novel first-order current-mode all-pass filters, employing minimal number of active and passive components i.e. single MO-DXCCII, one resistor and one capacitor. The proposed circuits show low input and high output impedance feature, which is desirable for the CM circuits. Beside this, the circuits employ grounded capacitor which is good for IC implementation in CMOS technology. No passive component matching constraints are required for the proposed filters. The simulation results confirm the workability of the proposed filters.

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